

FIG 1. General Block Diagram of the Self-Configuring Processing Element

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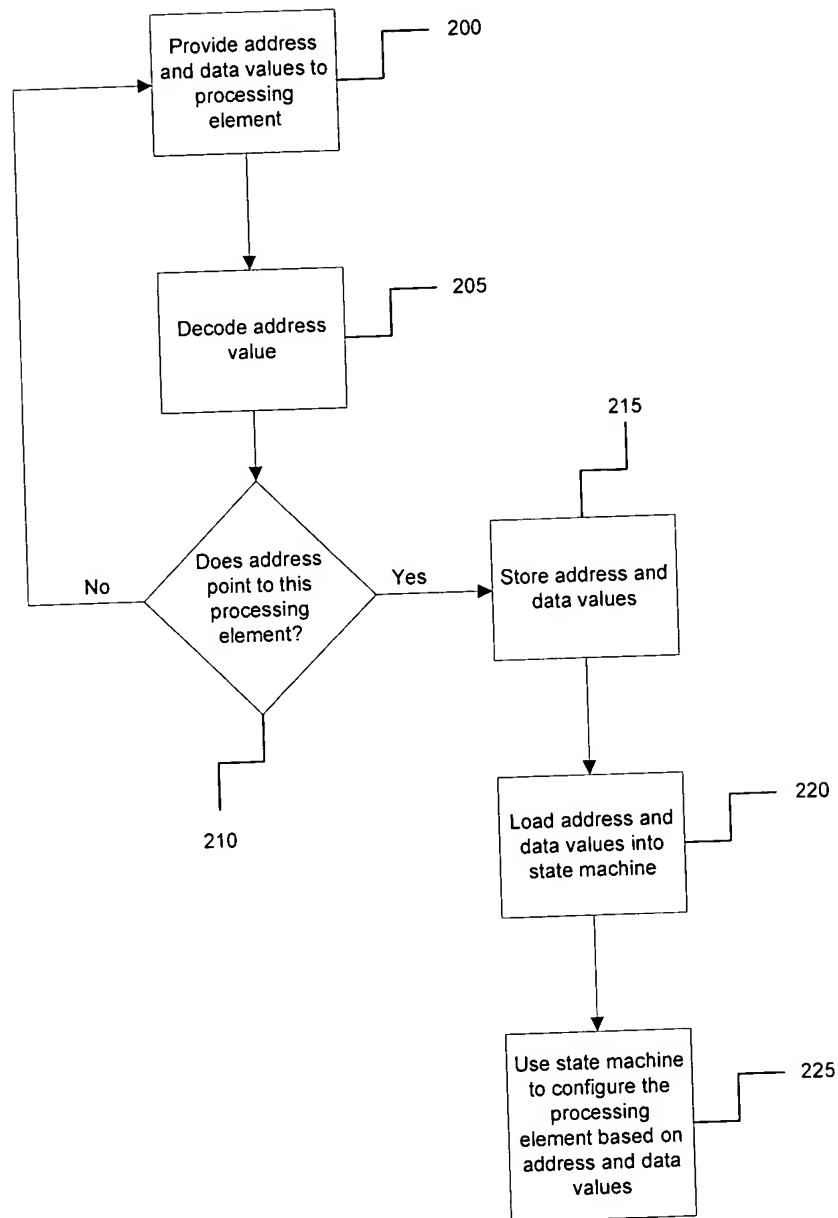


FIG. 2

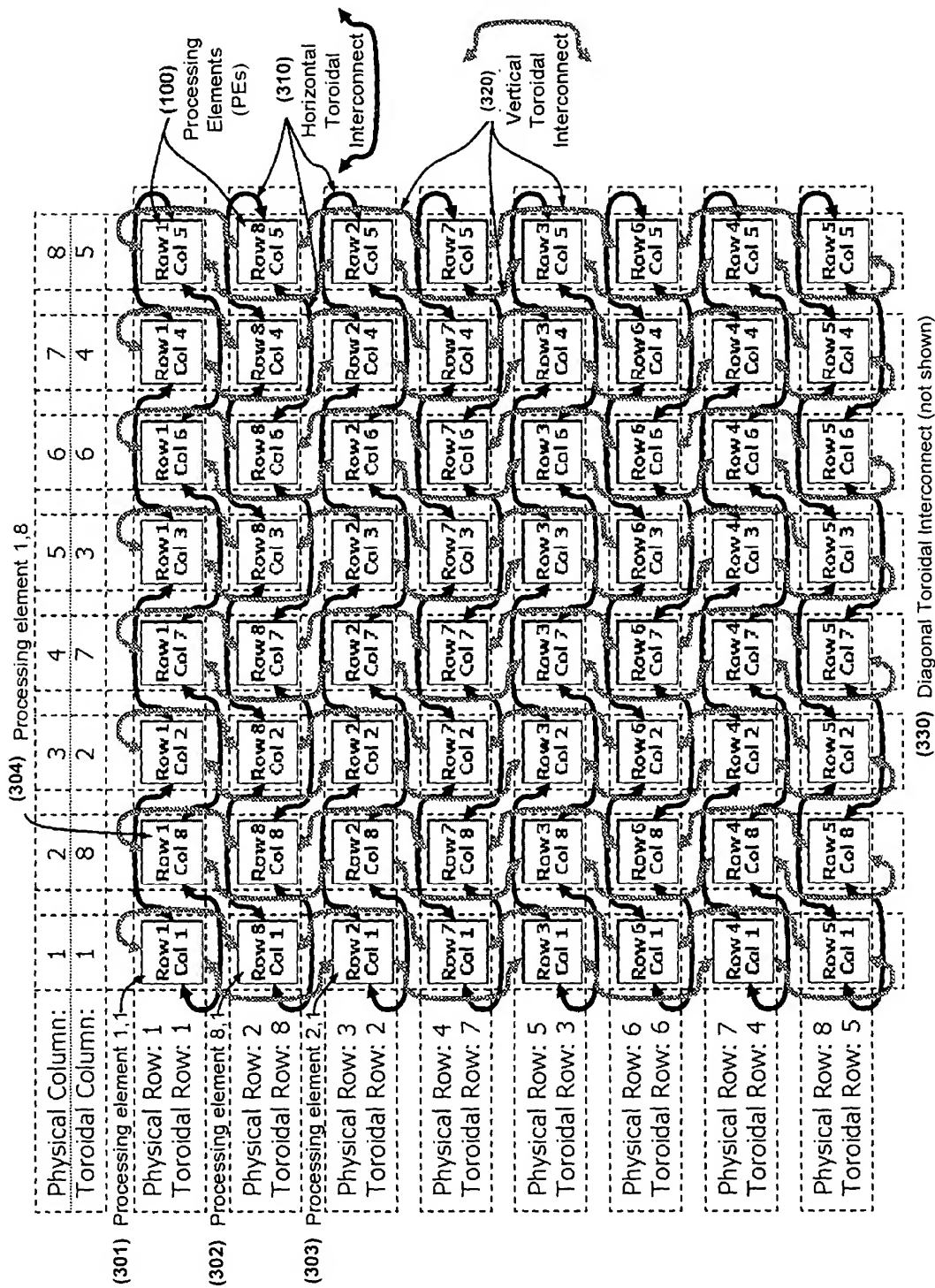


FIG. 3